

LEDGER FOR VERIFICATION, NOT FOR DISTRIBUTION TO STUDENTS.

DATE : 13 JAN. 2021

CENTRE : V.P. COLLEGE OF ENGINEERING, BARAMATI

PAGE NO. 01 (21)

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80353001 SANKPAL SAGAR NANAJI SHARDA , 71345448C , S80353002 , VPCOE , S80353001					
01. SIGNAL AND SYSTEMS	PP	100	40	55	P C
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C
04. SOLID STATES DEVICES AND CIRCUITS	PR	50	20	21	P C
05. NETWORK ANALYSIS	PP	100	40	40	P C
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C
07. DIGITAL LOGIC DESIGN	PR	50	20	32	P C
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C
09. NETWORK AND POWER LAB.	TW	50	20	39	P C
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C
11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C
12. ENGINEERING MATHEMATICS III	TW	25	10	15	P C
13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	65	P C
14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	22	P C
15. ELECTROMAGNETIC	PP	100	40	43	P
16. ELECTROMAGNETIC	TW	25	10	19	P C
17. DATA STRUCTURES	PP	100	40	40	P C
18. DATA STRUCTURES	PR	50	20	21	P C
19. COMMUNICATION THEORY	PP	100	40	40	P C
20. COMMUNICATION THEORY	OR	50	20	30	P C
21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P C

GRAND TOTAL = 739/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :